

GES'S R. H. Sapat College of Engineering, Management Studies and Research, Nashik -05
Department of Electronics and Telecommunication Engineering

Tentative Time Table 2024-25 SEM-I
Class - SE(E&TC)

Version : 3

w.e.f. : 22 July 2024

Day	MON	TUE	WED	THU	FRI	SAT
Time						
10:15am to 11:15am	DC (DMR)	EXC (MPJ)	DS (ASP)	S1- AC3, S2-ESD (PDL) , S3-AC3	DS (ASP)	ESTA Activity
11:15am to 12:15pm	EXC (MPJ)	ELC (KVK)	M-III (TUT) (SSS)		M-III (SSS)	
12:15pm to 1:00pm	BREAK					
1:00pm to 2:00pm	M-III (SSS)	M-III (SSS)	S1-ELC (KVK), S2- AC3, S3-ESD (PDL)	M-III (SSS)	DC (DMR)	
2:00pm to 3:00pm	ELC (KVK)	DS (ASP)		DC (DMR)	EXC (MPJ)	
3:00pm to 3:15pm	BREAK					
3:15pm to 4:15pm	S1-ESD (PDL), S2-DC (DMR) , S3-DS (ASP)	S1-DC (DMR), S2- DS (ASP) , S3-EXC (MPJ)	ELC (KVK)	S1-EXC (MPJ), S2-ELC (KVK), S3-DC (DMR)	S1- DS (ASP), S2-EXC (MPJ), S3-ELC	
4:15pm to 5:15pm			Library Hr			

Class Co-ordinator
Mrs. G. R. Kawale

Class Room No.
404

Subject Name	Subject Code	Teaching Scheme (Hours/ Week) (TH/PR/TUT)	Initials of Faculty		
MIII	Engineering Mathematics III	207005	4+1	SSS	Mr. S. S. Suryawanshi
EXC	Electronic Circuits	204181	3+2	MPJ	Dr. M. P. Joshi
DC	Digital Circuits	204182	3+2	DMR	Prof. D. M. Raut
ELC	Electrical Circuits	204183	3+2	KVK	Dr. K. V. Karad
DS	Data structures	204184	3+2	ASP	Dr. A. S. Patel
ESD	Electronic Skill Development	204189	2	PDL	Mr. P. D. Lokhande
AC3	Mandatory Audit Course 3	204190	1	GRK	Mrs. G. R. Kawale

Total Load = 27
 Modified load = 30*

Practical	Lab Location
EXC	Electronic Devices and Mechatronics Lab
DC	Electronic Instrumentation Lab
DS	VLSI Lab
ELC	Electrical Dept. Lab/ Power Lab
ESD	Microcontroller/ EDM Lab

Mr. P. D. Lokhande
Time Table I/C

Mrs. G. R. Kawale
Class Co-ordinator

Mr. D. M. Raut
Academic Coordinator

Dr. S. P. Agnihotri
HOD E & TC